



09-19-05

2012

PTO/SB/21 (09-04)

Approved for use through 07/31/2006. OMB 0651-0031

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(to be used for all correspondence after initial filing)

Application Number	09/292,132
Filing Date	April 14, 1999
First Named Inventor	Salman Akram, et al.
Art Unit	2812
Examiner Name	Savitri Malpuri
Attorney Docket Number	MI22-1171

Total Number of Pages in This Submission

ENCLOSURES (Check all that apply)

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	Wells St. John. P.S.		
Signature			
Printed name	D. Brent Kenady		
Date	September 16, 2005	Reg. No.	40,045

CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:

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EV633263788

Application Serial No. 09/292,132
Response to June 16, 2005 Office Action

MI22-1171



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/292,132
Filing Date April 14, 1999
Inventor Salman Akram et al.
Assignee Micron Technology, Inc.
Group Art Unit 2812
Examiner S. Mulpuri
Attorney's Docket No. MI22-1171
Title: Methods of Forming A Transistor Gate

RESPONSE TO JUNE 16, 2005 OFFICE ACTION

To: Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

From: D. Brent Kenady
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Responsive to the Office Action dated June 16, 2005, Applicant amends
and remarks as follows:

AMENDMENTS

Underlines indicate insertions and ~~strikeouts~~ indicate deletions.

EV633263788